

[WIRELESS COMMUNICATION CIRCUIT ARCHITECTURE]

Abstract of Disclosure

A wireless communication circuit has a first and second antennas. The second antenna is also set for use as a transmitting antenna. An antenna switch respectively receives signals from the first and second antennas and selects one of the signals. A first filter is used to receive the output signal from the antenna switch. A RFIC unit is used to receive an output signal from the first filter during the receiving mode as well as output a transmitting signal during the transmitting mode. A power amplifier is used to receive the signal and amplify the signal. A second filter receives the amplified transmitting signal to filter away an undesired frequency noise. Also and, a transmission/receiving (T/R) switch receives the transmitting signal from the second filter, wherein the T/R switch can also be switched to allow the signal received from the second antenna to be output to the second terminal of the antenna switch.

Figures